| | Type | L# | Hits | Search Text |
|-----|------|-----|------|--|
| 1 . | BRS | L1 | 18 | queue WITH (put\$3 enqueue\$3) WITH commit |
| 2 | BRS | L2 | 20 | queue SAME (put\$3 enqueue\$3) WITH commit |
| 3 | BRS | L3 | 2 | 2 not 1 |
| 4 | BRS | L4 | 20 | queue SAME (put\$3 enqueue\$3) WITH commit |
| 5 | BRS | L5 | 1 | queue SAME (put\$3 enqueue\$3) WITH commit |
| 6 | BRS | L6 | 2 | (put\$3 enqueue\$3) WITH commit |
| 7 | BRS | L7 | 27 | (put\$3 enqueue\$3) SAME commit |
| 8 | BRS | L8 | 10 | (index\$4 link\$4 point\$4) SAME (put\$3 enqueue\$3) SAME commit |
| 9 | BRS | L10 | 1 | (index\$4 link\$4 point\$4) SAME (put\$3 enqueue\$3) WITH commit.clm. |
| 10 | BRS | L9 | 14 | (index\$4 link\$4 point\$4) SAME (put\$3 enqueue\$3) WITH commit |
| 11 | BRS | L11 | 22 | (index\$4 link\$4 point\$4) SAME (put\$3 enqueue\$3) WITH commit |
| 12 | BRS | L12 | 4 | (index\$4 link\$4 pointer pointing) SAME (put\$3 enqueue\$3) WITH commit |
| 13 | BRS | L13 | 270 | (index\$4 link\$4 pointer pointing) WITH commit |
| 14 | BRS | L14 | 31 | ((index\$4 link\$4 pointer pointing) WITH commit).ti,ab,clm. |
| 15 | BRS | L15 | 2233 | 707/8 |
| 16 | BRS | L16 | 44 | 13 and 15 |
| 17 | BRS | L17 | 36 | 16 not 14 |
| 18 | BRS | L18 | 199 | (index\$4 link\$4 pointer pointing) WITH commit |
| 19 | BRS | L19 | 22 | (index\$4 link\$4 pointer pointing) WITH commit.clm. |
| 20 | BRS | L20 | 61 | 707/8 |
| 21 | BRS | L21 | 1 | 18 and 20 |
| 22 | BRS | L22 | 342 | (key index\$4 link\$4 pointer pointing) WITH commit |
| 23 | BRS | L23 | 1 | 22 and 20 |
| 24 | BRS | L24 | 29 | (key index\$4 link\$4 pointer pointing) WITH commit.clm. |

| | DBs | Time Stamp | Comments | Error Definition | Error |
|----|----------------------------------|------------------|----------|------------------|-------|
| 1 | USPAT | 2005/11/30 08:47 | | | |
| 2 | USPAT | 2005/11/30 09:19 | | | |
| 3 | USPAT . | 2005/11/30 09:20 | | | |
| 4 | US-PGPUB | 2005/11/30 09:20 | | | |
| 5 | EPO; JPO; DERWENT; IBM_TDB | 2005/11/30 09:52 | | | |
| 6 | EPO; JPO; DERWENT; IBM_TDB | 2005/11/30 09:53 | | | |
| 7 | EPO; JPO; DERWENT; IBM_TDB | 2005/11/30 09:59 | | | |
| 8 | EPO; JPO; DERWENT; IBM_TDB | 2005/11/30 11:19 | | | |
| 9 | US-PGPUB | 2005/11/30 11:20 | | | |
| 10 | US-PGPUB | 2005/11/30 11:25 | | | |
| 11 | USPAT | 2005/11/30 11:26 | | | |
| 12 | USPAT | 2005/11/30 11:35 | | | |
| 13 | USPAT | 2005/11/30 13:08 | | | |
| 14 | USPAT | 2005/11/30 12:26 | | | |
| 15 | USPAT | 2005/11/30 13:16 | | | |
| 16 | USPAT | 2005/11/30 13:17 | | | |
| 17 | USPAT | 2005/11/30 12:28 | | | |
| 18 | US-PGPUB | 2005/11/30 13:03 | | | |
| 19 | US-PGPUB | 2005/11/30 13:05 | | | |
| 20 | US-PGPUB | 2005/11/30 13:10 | | | |
| 21 | US-PGPUB | 2005/11/30 13:06 | | | |
| 22 | US-PGPUB | 2005/11/30 13:09 | | | |
| 23 | US-PGPUB | 2005/11/30 13:14 | | | |
| 24 | US-PGPUB | 2005/11/30 13:06 | | | |

| | Type | L# | Hits | Search Text |
|----|------|-----|------|---|
| 25 | BRS | L25 | 171 | (key) WITH commit |
| 26 | BRS | L26 | 825 | (key index\$4 link\$4 pointer pointing) SAME commit |
| 27 | BRS | L27 | 2667 | 707/1,8.ccls. |
| 28 | BRS | L28 | 126 | 707/8.ccls. |
| 29 | BRS | L29 | 44 | 26 and 27 |
| 30 | BRS | L30 | 20 | 22 and 27 |
| 31 | BRS | L31 | 0 | 707/8.ccls |
| 32 | BRS | L32 | 811 | 707/8.ccls. |
| 33 | BRS | L33 | 30 | 13 and 32 |
| 34 | BRS | L34 | 7 | 33 not (16 14) |
| 35 | BRS | L35 | 15 | 25 and 32 |
| 36 | BRS | L36 | 11 | 35 not (16 14 33) |

| | DBs | Time Stamp | Comments | Error Definition | Error s |
|----|----------|------------------|----------|------------------|------------|
| 25 | USPAT | 2005/11/30 13:15 | | | |
| 26 | US-PGPUB | 2005/11/30 13:09 | | | |
| 27 | US-PGPUB | 2005/11/30 13:10 | | | |
| 28 | US-PGPUB | 2005/11/30 13:10 | | | |
| 29 | US-PGPUB | 2005/11/30 13:11 | | | |
| 30 | US-PGPUB | 2005/11/30 13:15 | | | |
| 31 | USPAT | 2005/11/30 13:16 | | | |
| 32 | USPAT | 2005/11/30 13:16 | | | |
| 33 | USPAT | 2005/11/30 13:19 | | | |
| 34 | USPAT | 2005/11/30 13:18 | | | |
| 35 | USPAT | 2005/11/30 13:20 | | | |
| 36 | USPAT | 2005/11/30 13:20 | | | |



PALMINTRANET

Day: Wednesday Date: 11/30/2005 Time: 15:11:33

Inventor Name Search Result

Your Search was:

Last Name = SIDDALL First Name = PETER

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|------------------|---------------|--------|------------|---|----------------|
| 09579677 V | Not Issued | 41 | 05/26/2000 | Administration of groups of computer programs, data processing systems, or system resources | SIDDALL, PETER |
| <u>09605589</u> | Not Issued | 161 | 06/28/2000 | Method and apparatus for operating a computer system to enable a restart | SIDDALL, PETER |
| 09725578 V | 6665814 | 150 | 11/29/2000 | METHOD AND APPARATUS FOR PROVIDING SERIALIZATION SUPPORT FOR A COMPUTER SYSTEM | SIDDALL, PETER |
| 09790414 | Not Issued | 83 | 02/21/2001 | Data processing system and method | SIDDALL, PETER |
| 09790415 i | 6754842 | 150 | 02/21/2001 | FACILITATING A RESTART OPERATION WITHIN A DATA PROCESSING SYSTEM | SIDDALL, PETER |
| 09909538 VAPD | Not Issued | 71 | | Implementing MQI indexed queue support using coupling facility list structures | SIDDALL, PETER |
| <u>09912279</u> | 6842763 | 150 | | METHOD AND APPARATUS FOR IMPROVING MESSAGE AVAILABILITY IN A SUBSYSTEM WHICH SUPPORTS SHARED MESSAGE QUEUES | SIDDALL, PETER |
| 10228615 | 6848037 | 150 | 08/27/2002 | DATA PROCESSING ARRANGEMENT AND METHOD | SIDDALL, PETER |
| 10228636 | 6948093 | 150 | | DATA PROCESSING ARRANGEMENT AND METHOD | SIDDALL, PETER |
| 10256093 | Not Issued | 95 | 09/26/2002 | DATA RECOVERY SYSTEM | SIDDALL, PETER |
| 10660010 | Not Issued | 30 | | Recovery from failures within data processing systems | SIDDALL, PETER |
| 60219889 | Not Issued | 159 | | Implementing mqi indexed queue support using coupling facility structures | SIDDALL, PETER |
| 60220685 | Not Issued | 159 | | Method and apparatus for improving message availability in a subsystem which supports shared message queues | SIDDALL, PETER |

Inventor Search Completed: No Records to Display.



PALM INTRANET

Day: Wednesday Date: 11/30/2005 Time: 15:04:41

Inventor Name Search Result

Your Search was:

Last Name = NICK

First Name = JEFFREY

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|-----------------------|----------------|--------|------------|--|------------------|
| 09677338 V | Not Issued | 61 | 10/02/2000 | Method and apparatus for enforcing capacity limitations in a logically partitioned system | NICK, JEFFREY M. |
| v ⁰⁹⁶⁷⁷³³⁹ | 6963882 | 150 | 10/02/2000 | METHOD AND APPARATUS FOR PROCESSING A LIST STRUCTURE | NICK, JEFFREY M. |
| 09677341 | 6862595 | 150 | 10/02/2000 | METHOD AND APPARATUS FOR IMPLEMENTING A SHARED MESSAGE QUEUE USING A LIST STRUCTURE | NICK, JEFFREY M. |
| 09677454 V | 6854021 | 150 | 10/02/2000 | COMMUNICATIONS BETWEEN PARTITIONS WITHIN A LOGICALLY PARTITIONED COMPUTER | NICK, JEFFREY M. |
| v ⁹⁹⁸⁰¹⁹⁹³ | Not Issued | 95 | 03/08/2001 | INTER-PARTITION MESSAGE PASSING METHOD, SYSTEM AND PROGRAM PRODUCT FOR MANAGING WORKLOAD IN A PARTITIONED PROCESSING ENVIRONMENT | NICK, JEFFREY M. |
| 09909538 A-PP | Not Issued | 71 | | Implementing MQI indexed queue support using coupling facility list structures | NICK, JEFFREY M. |
| 09968179 | <u>6859866</u> | 150 | 10/01/2001 | SYNCHRONIZING PROCESSING OF COMMANDS INVOKED AGAINST DUPLEXED COUPLING FACILITY STRUCTURES | NICK, JEFFREY M. |
| 09968185 V | Not Issued | 71 | | Dynamically determining whether to process requests synchronously or asynchronously | NICK, JEFFREY M. |
| 09968242 | 6944787 | 150 | | SYSTEM-MANAGED DUPLEXING OF COUPLING FACILITY STRUCTURES | NICK, JEFFREY M. |
| | <u>6954817</u> | 150 | | PROVIDING AT LEAST ONE PEER CONNECTION BETWEEN A PLURALITY OF COUPLING FACILITIES TO COUPLE THE PLURALITY OF COUPLING FACILITIES | NICK, JEFFREY M. |
| 09968248 | Not Issued | 94 | | MANAGING THE STATE OF COUPLING FACILITY STRUCTURES | NICK, JEFFREY M. |
| N 10116985 | Not | 95 | 04/05/2002 | MANAGING PROCESSING | NICK, JEFFREY M. |

| | Issued | | | ASSOCIATED WITH COUPLING FACILITY STRUCTURES | |
|-----------------|---------------|-----|------------|--|------------------|
| 10118113 | 6963994 | 150 | 04/05/2002 | MANAGING CONNECTIONS TO COUPLING FACILITY STRUCTURES | NICK, JEFFREY M. |
| 10140622 | Not Issued | 30 | 05/08/2002 | Controlling the state of duplexing of coupling facility structures | NICK, JEFFREY M. |
| 10141040 | 6615373 | 150 | 05/08/2002 | METHOD, SYSTEM AND PROGRAM PRODUCTS FOR RESOLVING POTENTIAL DEADLOCKS | NICK, JEFFREY M. |
| 11220296 | Not Issued | 20 | 09/06/2005 | Method, system and program products for managing logical processors of a computing environment | NICK, JEFFREY M. |
| 60219889 ✓ | Not Issued | 159 | | Implementing mqi indexed queue support using coupling facility structures | NICK, JEFFREY M. |
| 07221169 | 4979105 | 250 | 07/19/1988 | METHOD AND APPARATUS FOR AUTOMATIC RECOVERY FROM EXCESSIVE SPIN LOOPS IN AN N- WAY MULTIPROCESSING SYSTEM | NICK, JEFFREY M. |
| 07754816 | Not Issued | 166 | 09/04/1991 | METHOD AND APPARATUS FOR RAPID DATA COPYING USING REASSIGNED BACKING PAGES | NICK, JEFFREY M. |
| 07860330 | 5339405 | 150 | 03/30/1992 | COMMAND QUIESCE FUNCTION | NICK, JEFFREY M. |
| <u>07860378</u> | 5392397 | 150 | 03/30/1992 | EXECUTION SYSTEM FOR USING FIRST AND SECOND COMMANDS TO RESERVE AND STORE SECOND COMMAND RELATED STATUS INFORMATION IN MEMORY PORTION RESPECTIVELY | NICK, JEFFREY M. |
| <u>07860380</u> | Not Issued | 166 | 03/30/1992 | IN A MULTIPROCESSING SYSTEM HAVING A COUPLING FACILITY, COMMUNICATING MESSAGES BETWEEN THE PROCESSORS AND THE COUPLING FACILITY IN EITHER A SYNCHRONOUS OPERATION OR AN ASYNCHRONOUS OPERATION | NICK, JEFFREY M. |
| 07860489 V | 5394554 | 150 | | INTERDICTING I/O AND MASSAGING OPERATIONS FROM SENDING CENTRAL PROCESSING COMPLEX TO OTHER CENTRAL PROCESSING COMPLEXES AND TO I/O DEVICE IN MULTI-SYSTEM COMPLEX | NICK, JEFFREY M. |
| 07860633 V | 5410695 | 150 | | APPARATUS AND METHOD FOR LIST MANAGEMENT IN A COUPLED DATA PROCESSING SYSTEM | NICK, JEFFREY M. |
| 07860646 | Not Issued | 166 | | MESSAGE PATH MECHANISM FOR MANAGING CONNECTIONS BETWEEN PROCESSORS AND A COUPLING FACILITY | NICK, JEFFREY M. |
| 07860647 | 5394542 | 150 | 03/30/1992 | CLEARING DATA OBJECTS USED TO | NICK, JEFFREY M. |

| V | | | | MAINTAIN STATE INFORMATION FOR SHARED DATA AT A LOCAL COMPLEX WHEN AT LEAST ONE MESSAGE PATH TO THE LOCAL COMPLEX CANNOT BE RECOVERED | |
|---------------|---------------|-----|------------|---|------------------|
| 07860655 | Not Issued | 166 | 03/30/1992 | METHOD AND APPARATUS FOR PERFORMING CONDITIONAL OPERATIONS ON EXTERNALLY SHARED DATA | NICK, JEFFREY M. |
| 07860797 | 5388266 | 250 | 03/30/1992 | MANAGEMENT OF DATA OBJECTS USED TO MAINTAIN STATE INFORMATION FOR SHARED DATA AT A LOCAL COMPLEX | NICK, JEFFREY M. |
| 07860800 | 5331673 | 150 | 03/30/1992 | INTEGRITY OF DATA OBJECTS USED TO MAINTAIN STATE INFORMATION FOR SHARED DATA AT A LOCAL COMPLEX | NICK, JEFFREY M. |
| 07860803 | 5317739 | 150 | 03/30/1992 | METHOD AND APPARATUS FOR COUPLING DATA PROCESSING SYSTEMS | NICK, JEFFREY M. |
| 07860805 | 5537574 | 150 | 03/30/1992 | SYSPLEX SHARED DATA COHERENCY METHOD | NICK, JEFFREY M. |
| 07860806 2 | 5493668 | 150 | 03/30/1992 | MULTIPLE PROCESSOR SYSTEM HAVING SOFTWARE FOR SELECTING SHARED CACHE ENTRIES OF AN ASSOCIATED CASTOUT CLASS FOR TRANSFER TO A DASD WITH ONE I/O OPERATION | NICK, JEFFREY M. |
| 07860807 | 5457793 | 150 | | SOFTWARE CACHE MANAGEMENT OF A SHARED ELECTRONIC STORE IN A SYSPLEX | NICK, JEFFREY M. |
| 07860809 | 5390328 | 150 | | DATA PROCESSING SYSTEM AND METHOD FOR PROVIDING NOTIFICATION TO A CENTRAL PROCESSOR OF STATE CHANGES FOR SHARED DATA STRUCTURE ON EXTERNAL STORAGE | NICK, JEFFREY M. |
| 07886273 | Not Issued | 166 | | METHOD AND SYSTEM FOR LOCKING A PAGE OF REAL STORAGE USING A VIRTUAL ADDRESS | NICK, JEFFREY M. |
| 08021285 | Not Issued | 166 | | AUTHORIZATION METHOD FOR CONDITIONAL COMMAND EXECUTION | NICK, JEFFREY M. |
| 08073909 | 5761739 | 150 | 06/08/1993 | METHODS AND SYSTEMS FOR CREATING A STORAGE DUMP WITHIN A COUPLING FACILITY OF A MULTISYSTEM ENVIRONMENT | NICK, JEFFREY M. |
| 08146635 | Not Issued | 166 | II I | METHOD AND SYSTEM FOR RECONFIGURING A STORAGE | NICK, JEFFREY M. |

| | | | | STRUCTURE LOCATED WITHIN A STRUCTURE PROCESSING FACILITY | |
|----------|----------------|-----|------------|--|------------------|
| 08146647 | 5630050 | 150 | 11/01/1993 | METHOD AND SYSTEM FOR CAPTURING AND CONTROLLING ACCESS TO INFORMATION IN A COUPLING FACILITY | NICK, JEFFREY M. |
| 08146727 | 5465359 | 150 | 11/01/1993 | METHOD AND SYSTEM FOR MANAGING DATA AND USERS OF DATA IN A DATA PROCESSING SYSTEM | NICK, JEFFREY M. |
| 08147351 | 5416921 | 150 | 11/03/1993 | APPARATUS AND ACCOMPANYING METHOD FOR USE IN A SYSPLEX ENVIRONMENT FOR PERFORMING ESCALATED ISOLATION OF A SYSPLEX COMPONENT IN THE EVENT OF A FAILURE | NICK, JEFFREY M. |
| 08148707 | 5544345 | 150 | 11/08/1993 | COHENRENCE CONTROLS FOR STORE-MULTIPLE SHARED DATA COORDINATED BY CACHE DIRECTORY ENTRIES IN A SHARED ELECTRONIC STORAGE | NICK, JEFFREY M. |
| 08276512 | 5394539 | 250 | 07/15/1994 | METHOD AND APPARATUS FOR RAPID DATA COPYING REASSIGNED BACKING PAGES | NICK, JEFFREY M. |
| 08304458 | 5581737 | 150 | 09/12/1994 | METHOD AND APPARATUS FOR EXPANSION, CONTRACTION, AND REAPPORTIONMENT OF STRUCTURED EXTERNAL STORAGE STRUCTURES | NICK, JEFFREY M. |
| 08304677 | Not Issued | 166 | | METHOD AND SYSTEM FOR LOG MANAGEMENT IN A COUPLED DATA PROCESSING SYSTEM | NICK, JEFFREY M. |
| 08324447 | 5463736 | 150 | 10/18/1994 | COUPLING FACILITY FOR RECEIVING COMMANDS FROM PLURALITY OF HOSTS FOR ACTIVATING SELECTED CONNECTION PATHS TO I/O DEVICES AND MAINTAING STATUS THEREOF | NICK, JEFFREY M. |
| 08383532 | 5742830 | 150 | 02/01/1995 | METHOD AND APPARATUS FOR PERFORMING CONDITIONAL OPERATIONS ON EXTERNALLY SHARED DATA | NICK, JEFFREY M. |
| 08408446 | 5450590 | 150 | 03/22/1995 | AUTHORIZATION METHOD FOR CONDITIONAL COMMAND EXECUTION | NICK, JEFFREY M. |
| 08420893 | <u>5561809</u> | 150 | | IN A MULTIPROCESSING SYSTEM HAVING A COUPLING FACILITY, COMMUNICATING MESSAGES BETWEEN THE PROCESSORS AND | NICK, JEFFREY M. |

| | | | THE COUPLING FACILITY IN EITHER A SYNCHRONOUS OPERATION OR AN ASYNCHRONOUS OPERATION | |
|----------|---------|-----|--|------------------|
| 08439269 | 5604863 | 150 | A METHOD FOR COORDINATING EXECUTING PROGRAMS IN A DATA PROCESSING SYSTEM | NICK, JEFFREY M. |

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Inventor Name Search Result

Your Search was:

Last Name = WARNES First Name = JAMES

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---------------------------|---------------|--------|------------|---|------------------------|
| v ⁶⁰²²⁰⁶⁸⁵ | Not Issued | 159 | 07/25/2000 | Method and apparatus for improving message availability in a subsystem which supports shared message queues | WARNES, JAMES |
| <u>√09677339</u> | 6963882 ~ | 150 | 10/02/2000 | METHOD AND APPARATUS FOR PROCESSING A LIST STRUCTURE | WARNES, JAMES H. |
| 09677341 | 6862595 | 150 | 10/02/2000 | METHOD AND APPARATUS FOR IMPLEMENTING A SHARED MESSAGE QUEUE USING A LIST STRUCTURE | WARNES, JAMES H. |
| 09909538 isfl | Not Issued | 71 | 07/20/2001 | Implementing MQI indexed queue support using coupling facility list structures | WARNES, JAMES H. |
| 09912279 | 6842763 | 150 | 07/24/2001 | METHOD AND APPARATUS FOR IMPROVING MESSAGE AVAILABILITY IN A SUBSYSTEM WHICH SUPPORTS SHARED MESSAGE QUEUES | WARNES, JAMES H. |
| VH1005805 | Not Issued | 30 | 12/07/2004 | Browsing a list of data items | WARNES, JAMES H. |
| 60219889 PLIOLETY NOC. | Not Issued | 159 | 07/21/2000 | Implementing mqi indexed queue support using coupling facility structures | WARNES, JAMES H. |
| <u>06781844</u> | 4809157 | 150 | 09/30/1985 | DYNAMIC ASSIGNMENT OF AFFINITY FOR VECTOR TASKS | WARNES, JAMES H. |
| <u>08304677</u> | Not Issued | 166 | 09/12/1994 | METHOD AND SYSTEM FOR LOG MANAGEMENT IN A COUPLED DATA PROCESSING SYSTEM | WARNES, JAMES H. |
| 08632683 | 5737600 | 150 | 04/15/1996 | METHOD AND SYSTEM FOR LOG MANAGEMENT IN A COUPLED DATA PROCESSING SYSTEM | WARNES, JAMES H. |
| 09725578 V | 6665814 | 150 | 11/29/2000 | METHOD AND APPARATUS FOR PROVIDING SERIALIZATION SUPPORT FOR A COMPUTER SYSTEM | WARNES, JAMES HENRY |

Inventor Search Completed: No Records to Display.

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WARNES JAMES Search

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Inventor Name Search Result

Your Search was:

Last Name = HOPEWELL

First Name = PAUL

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|---------------------------|---------------|--------|------------|---|-------------------------|
| , <u>09909538</u> HPP | Not Issued | 71 | | Implementing MQI indexed queue support using coupling facility list structures | HOPEWELL, PAUL |
| 10228615 | 6848037 | 150 | 08/27/2002 | DATA PROCESSING ARRANGEMENT AND METHOD | HOPEWELL, PAUL |
| 10228636 | 6948093 | 150 | 08/27/2002 | DATA PROCESSING ARRANGEMENT AND METHOD | HOPEWELL, PAUL |
| 10256093 | Not Issued | 95 | 09/26/2002 | DATA RECOVERY SYSTEM | HOPEWELL, PAUL |
| 10660010 | Not Issued | 30 | | Recovery from failures within data processing systems | HOPEWELL, PAUL |
| 11005805 | Not Issued | 30 | 12/07/2004 | Browsing a list of data items | HOPEWELL, PAUL |
| 11255204 | Not Issued | 20 | | Method, apparatus, computer program and computer program product for adjusting the frequency at which data is backed up | HOPEWELL, PAUL |
| 60219889 Parakery Dock | Not Issued | 159 | 07/21/2000 | Implementing mqi indexed queue support using coupling facility structures | HOPEWELL, PAUL |
| 11144931 | Not Issued | 19 | | System and method for operating a wind farm under high wind speed conditions | HOPEWELL, PAUL DAVID |
| V 11172769 | Not Issued | 30 | | System and method for controlling effective wind farm power output | HOPEWELL, PAUL DAVID |

Inventor Search Completed: No Records to Display.

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HOPEWELL

PAUL

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PALM INTRANET

Day: Wednesday Date: 11/30/2005 Time: 14:49:00

Inventor Name Search Result

Your Search was:

Last Name = KETTLEY

First Name = PAUL

| Application# | Patent# | Status | Date Filed | Title | Inventor Name |
|----------------------|---------------|-------------|------------|---|---------------|
| <u>09579677</u> | Not Issued | 41 | 05/26/2000 | Administration of groups of computer programs, data processing systems, or system resources | KETTLEY, PAUL |
| 09605589 | Not Issued | <u>(61)</u> | 06/28/2000 | Method and apparatus for operating a computer system to enable a restart | KETTLEY, PAUL |
| <u>09725578</u> √ | 6665814 | 150 | 11/29/2000 | METHOD AND APPARATUS FOR PROVIDING SERIALIZATION SUPPORT FOR A COMPUTER SYSTEM | KETTLEY, PAUL |
| 09790414 | Not Issued | 83 | 02/21/2001 | Data processing system and method | KETTLEY, PAUL |
| <u>09790415</u> | 6754842 | 150 | 02/21/2001 | FACILITATING A RESTART OPERATION WITHIN A DATA PROCESSING SYSTEM | KETTLEY, PAUL |
| 09909538 1~PP | Not Issued | 71 | | Implementing MQI indexed queue support using coupling facility list structures | KETTLEY, PAUL |
| 09912279 | 6842763 | 150 | 07/24/2001 | METHOD AND APPARATUS FOR IMPROVING MESSAGE AVAILABILITY IN A SUBSYSTEM WHICH SUPPORTS SHARED MESSAGE QUEUES | KETTLEY, PAUL |
| 10228615 | 6848037 | 150 | 08/27/2002 | DATA PROCESSING ARRANGEMENT AND METHOD | KETTLEY, PAUL |
| 10228636 | 6948093 | 150 | 08/27/2002 | DATA PROCESSING ARRANGEMENT AND METHOD | KETTLEY, PAUL |
| 10256093 | Not Issued | 95 | 09/26/2002 | DATA RECOVERY SYSTEM | KETTLEY, PAUL |
| 10660010 | Not Issued | 30 | | Recovery from failures within data processing systems | KETTLEY, PAUL |
| 11168689 | Not Issued | 30 | | Controlling a transmission cache in a networked file system | KETTLEY, PAUL |
| 11255204 | Not Issued | 20 | | Method, apparatus, computer program and computer program product for adjusting the frequency at which data is backed up | KETTLEY, PAUL |
| 60183861 | Not Issued | 159 | 02/22/2000 | Accelerating resource manager restart via force commit on incomplete units of work | KETTLEY, PAUL |
| | | | | | |

| 60183925 | Not Issued | 159 | | Flexible mechanism for controlling access to resources within a group of co-operating queue managers | |
|----------------------|---------------|-----|----|---|---------------|
| 60219889 Preonery | Not Issued | 159 | 51 | Implementing mqi indexed queue support using coupling facility structures | KETTLEY, PAUL |
| 60220685 | Not Issued | 159 | | Method and apparatus for improving message availability in a subsystem which supports shared message queues | KETTLEY, PAUL |

Inventor Search Completed: No Records to Display.

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Siddall R.

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Volume 13, Issue 1, Jan. 2005 Page(s):14 - 26 Digital Object Identifier 10.1109/TVLSI.2004.840406

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Interposed request routing for scalable network storage

February 2002 ACM Transactions on Computer Systems (TOCS), Volume 20 Issue 1

Publisher: ACM Press

Full text available: pdf(363.12 KB)

Additional Information: full citation, abstract, references, citings, index terms, review

This paper explores interposed request routing in Slice, a new storage system architecture for high-speed networks incorporating network-attached block storage. Slice interposes a request switching filter---called a µproxy---along each client's network path to the storage service (e.g., in a network adapter or switch). The uproxy intercepts request traffic and distributes it across a server ensemble. We propose request routing schemes for I/O and file service traffic, and explore th ...

Keywords: Content switch, file server, network file system, network storage, request redirection, service virtualization

Events in Haskell, and how to implement them

George Russell

October 2001 ACM SIGPLAN Notices, Proceedings of the sixth ACM SIGPLAN

international conference on Functional programming ICFP '01, Volume 36

Issue 10

Publisher: ACM Press

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We describe a new and simpler implementation in Haskell of CML's events, which encode reactions by a thread to combinations of messages from other threads. We add a new type of Guarded Events, by which recipients can filter messages with conditions on their value known as Guards. We implement guarded channels. The guard type and the indexing algorithm are not part of the channel definition, so that the user can trade off what guards are required against the cost of indexing. As an exampl ...

3 ARIES: a transaction recovery method supporting fine-granularity locking and partial



nollbacks using write-ahead logging

C. Mohan, Don Haderle, Bruce Lindsay, Hamid Pirahesh, Peter Schwarz March 1992 ACM Transactions on Database Systems (TODS), Volume 17 Issue 1

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DB2TM, IMS, and TandemTM systems. ARIES is applicable not only to database management systems but also to persistent object-oriented languages, recoverable file systems and transaction-based operating systems. ARIES has been implemented, to

varying degrees, in IBM's OS/2TM Extended Edition Database Manager, DB2, Workstation Data Save Facility/VM, Starburst and QuickSilver, and in the University of Wisconsin's EXODUS and Gamma d ...

Keywords: buffer management, latching, locking, space management, write-ahead logging

4 Cheap recovery: a key to self-managing state

Andrew C. Huang, Armando Fox

February 2005 ACM Transactions on Storage (TOS), Volume 1 Issue 1

Publisher: ACM Press

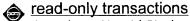
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Cluster hash tables (CHTs) are key components of many large-scale Internet services due to their highly-scalable performance and the prevalence of the type of data they store. Another advantage of CHTs is that they can be designed to be as self-managing as a cluster of stateless servers. One key to achieving this extreme manageability is reboot-based recovery that is predictably fast and has modest impact on system performance and availability. This "cheap" recovery mechanism simplifies manageme ...

Keywords: Cluster hash table, manageability, quourum replication, storage systems design

5 Efficient and flexible methods for transient versioning of records to avoid locking by

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C. Mohan, Hamid Pirahesh, Raymond Lorie

June 1992 ACM SIGMOD Record , Proceedings of the 1992 ACM SIGMOD international conference on Management of data SIGMOD '92, Volume 21 Issue 2

Publisher: ACM Press

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We present efficient and flexible methods which permit read-only transactions that do not mind reading a possibly slightly old, but still consistent, version of the data base to execute without acquiring locks. This approach avoids the undesirable interferences between such queries and the typically shorter update transactions that cause unnecessary and costly delays. Indexed access by such queries is also supported, unlike by the earlier methods. Old versions of records are maintained only ...

6 Efficient distributed recovery using message logging

A. P. Sistla, J. L. Welch

June 1989 Proceedings of the eighth annual ACM Symposium on Principles of distributed computing

Publisher: ACM Press

Full text available: 🔂 pdf(1.87 MB) Additional Information: full citation, references, citings, index terms

Research session: architectural issues: C-store: a column-oriented DBMS

Mike Stonebraker, Daniel J. Abadi, Adam Batkin, Xuedong Chen, Mitch Cherniack, Miguel
Ferreira, Edmond Lau, Amerson Lin, Sam Madden, Elizabeth O'Neil, Pat O'Neil, Alex Rasin,
Nga Tran, Stan Zdonik

August 2005 Proceedings of the 31st international conference on Very large data bases VLDB '05

Publisher: VLDB Endowment

Full text available: pdf(210.85 KB) Additional Information: full citation, abstract, references, index terms

This paper presents the design of a read-optimized relational DBMS that contrasts sharply with most current systems, which are write-optimized. Among the many differences in its design are: storage of data by column rather than by row, careful coding and packing of

objects into storage including main memory during query processing, storing an overlapping collection of column-oriented projections, rather than the current fare of tables and indexes, a non-traditional implementation of transactions ...

8 The family of concurrent logic programming languages

Ehud Shapiro

September 1989 ACM Computing Surveys (CSUR), Volume 21 Issue 3

Publisher: ACM Press

Full text available: pdf(9.62 MB)

Additional Information: <u>full citation</u>, <u>abstract</u>, <u>references</u>, <u>citings</u>, <u>index</u> terms

Concurrent logic languages are high-level programming languages for parallel and distributed systems that offer a wide range of both known and novel concurrent programming techniques. Being logic programming languages, they preserve many advantages of the abstract logic programming model, including the logical reading of programs and computations, the convenience of representing data structures with logical terms and manipulating them using unification, and the amenability to metaprogrammin ...

⁹ An object server for an object-oriented database system

Andrea H. Skarra, Stanley B. Zdonik, Stephen P. Reiss

September 1986 Proceedings on the 1986 international workshop on Object-oriented database systems

Publisher: IEEE Computer Society Press

Full text available: pdf(853.89 KB)

Additional Information: full citation, abstract, references, citings, index terms

This paper summarizes the interface, implementation, and use of a server process that is used as a backend by an object-oriented database system. This server is responsible for managing objects on secondary storage, managing transactions, and implementing a simple form of trigger. We sketch the interface of this system and point out some of the more interesting implementation issues that were encountered in building it. Client

processes communicate asynchronously with the server ...

10 Parallel multisource view maintenance

Xin Zhang, Lingli Ding, Elke A. Rundensteiner

January 2004 The VLDB Journal — The International Journal on Very Large Data

Bases, Volume 13 Issue 1

Publisher: Springer-Verlag New York, Inc.

Full text available: pdf(382.15 KB) Additional Information: full citation, abstract, index terms

In a distributed environment, materialized views are used to integrate data from different information sources and then store them in some centralized location. In order to maintain such materialized views, maintenance queries need to be sent to information sources by the data warehouse management system. Due to the independence of the information sources and the data warehouse, concurrency issues are raised between the maintenance queries and the local update transactions at each information so ...

Keywords: Concurrent data updates, Data warehousing, Parallel view maintenance, Performance evaluation

11 Practical byzantine fault tolerance and proactive recovery

Miguel Castro, Barbara Liskov

November 2002 ACM Transactions on Computer Systems (TOCS), Volume 20 Issue 4

Publisher: ACM Press

Full text available: pdf(1.63 MB)

Additional Information: full citation, abstract, references, citings, index terms, review

Our growing reliance on online services accessible on the Internet demands highly available systems that provide correct service without interruptions. Software bugs, operator mistakes, and malicious attacks are a major cause of service interruptions and they can cause arbitrary behavior, that is, Byzantine faults. This article describes a new

replication algorithm, BFT, that can be used to build highly available systems that tolerate Byzantine faults. BFT can be used in practice to implement re ...

Keywords: Byzantine fault tolerance, asynchronous systems, proactive recovery, state machine replication, state transfer

12 Scalable Load and Store Processing in Latency Tolerant Processors

Amit Gandhi, Haitham Akkary, Ravi Rajwar, Srikanth T. Srinivasan, Konrad Lai June 2005 Proceedings of the 32nd Annual International Symposium on Computer Architecture ISCA '05

Publisher: IEEE Computer Society

Full text available: pdf(187.74 KB) Additional Information: full citation, abstract

Memory latency tolerant architectures support thousands of in-flight instructions without scaling cycle-critical processor resources, and thousands of useful instructions can complete in parallel with a miss to memory. These architectures however require large queues to track all loads and stores executed while a miss is pending. Hierarchical designs alleviate cycle time impact of these structures but the CAM and search functions required to enforce memory ordering and provide data forwarding pl ...

13 Transient-fault recovery using simultaneous multithreading

T. N. Vijaykumar, Irith Pomeranz, Karl Cheng

May 2002 ACM SIGARCH Computer Architecture News, Proceedings of the 29th annual international symposium on Computer architecture ISCA '02, Proceedings of the 29th annual international symposium on Computer architecture ISCA '02, Volume 30 Issue 2

Publisher: IEEE Computer Society, ACM Press

We propose a scheme for transient-fault recovery called **Simultaneously and Redundantly Threaded processors with Recovery (SRTR)** that enhances a previously proposed scheme for transient-fault detection, called Simultaneously and Redundantly Threaded (SRT) processors. SRT replicates an application into two communicating threads, one executing ahead of the other. The trailing thread repeats the computation performed by the leading thread, and the values produced by the two threads are compar ...

14 Research papers: storage, indexing, and system architecture: Online B-tree merging

Xiaowei Sun, Rui Wang, Betty Salzberg, Chendong Zou

June 2005 Proceedings of the 2005 ACM SIGMOD international conference on Management of data

Publisher: ACM Press

Full text available: pdf(394.41 KB) Additional Information: full citation, abstract, references

Many scenarios involve merging of two B-tree indexes, both covering the same key range. Increasing demand for continuous availability and high performance requires that such merging be done online, with minimal interference to normal user transactions. In this paper we present an online B-tree merging method, in which the merging of leaf pages in two B-trees are piggybacked lazily with normal user transactions, thus making the merging I/O efficient and allowing user transactions to access only o ...

15 Parallelism in relational data base systems: architectural issues and design

approaches

Hamid Pirahesh, C. Mohan, Josephine Cheng, T. S. Liu, Pat Selinger

July 1990 Proceedings of the second international symposium on Databases in parallel and distributed systems

Publisher: ACM Press

Full text available: pdf(2.50 MB)

Additional Information: full citation, abstract, references, citings, index terms

With current systems, some important complex queries may take days to complete because of: (1) the volume of data to be processed, (2) limited aggregate resources. Introducing parallelism addresses the first problem. Cheaper, but powerful computing resources solve the second problem. According to a survey by Brodie,1 only 10% of computerized data is in data bases. This is an argument for both more variety and volume of data to be moved into data base systems. We conject ...

16 Computing curricula 2001

September 2001 Journal on Educational Resources in Computing (JERIC)

Publisher: ACM Press

Full text available: pdf(613.63 KB)

Additional Information: <u>full citation</u>, <u>references</u>, <u>citings</u>, <u>index terms</u>

17 An analysis of a resource efficient checkpoint architecture

Haitham Akkary, Ravi Rajwar, Srikanth T. Srinivasan

December 2004 ACM Transactions on Architecture and Code Optimization (TACO),

Volume 1 Issue 4

Publisher: ACM Press

Full text available: pdf(757.69 KB) Additional Information: full citation, abstract, references, index terms

Large instruction window processors achieve high performance by exposing large amounts of instruction level parallelism. However, accessing large hardware structures typically required to buffer and process such instruction window sizes significantly degrade the cycle time. This paper proposes a novel checkpoint processing and recovery (CPR) microarchitecture, and shows how to implement a large instruction window processor without requiring large structures thus permitting a high clock frequency ...

Keywords: Computer architecture, checkpoint architecture, high-performance computing, scalable architecture

18 Reducing Design Complexity of the Load/Store Queue

Il Park, Chong Liang Ooi, T. N. Vijaykumar

December 2003 Proceedings of the 36th annual IEEE/ACM International Symposium on Microarchitecture

Publisher: IEEE Computer Society

Full text available: pdf(174.73 KB) Additional Information: full citation, abstract, citings, index terms

With faster CPU clocks and wider pipelines, all relevantmicroarchitecture components should scale accordingly. There have been many proposals for scaling the issue queue, register file, and cache hierarchy. However, nothing has beendone for scaling the load/store queue, despite the increasing pressure on the load/store queue in terms of capacity and search bandwidth. The load/store queue is a CAM structurewhich holds inflight memory instructions and supports simultaneous searches to honor memory dep ...

19 WISQ: a restartable architecture using queues

A. R. Pleszkun, J. R. Goodman, W. C. Hsu, R. T. Joersz, G. Bier, P. Woest, P. B. Schechter June 1987 Proceedings of the 14th annual international symposium on Computer architecture

Publisher: ACM Press

Full text available: pdf(1.14 MB)

Additional Information: full citation, abstract, references, citings, index terms

In this paper, the WISQ architecture is described. This architecture is designed to achieve high performance by exploiting new compiler technology and using a highly segmented pipeline. By having a highly segmented pipeline, a very-high-speed clock can be used. Since a highly segmented pipeline will require relatively long pipelines, a way must be provided to minimize the effects of pipeline bubbles that are formed due to data and control dependencies. It is also important to provide a way ...



Multikey access methods based on superimposed coding techniques

R. Sacks-Davis, A. Kent, K. Ramamohanarao

November 1987 ACM Transactions on Database Systems (TODS), Volume 12 Issue 4

Publisher: ACM Press

Full text available: pdf(3.71 MB)

Additional Information: full citation, abstract, references, citings, index

terms, review

Both single-level and two-level indexed descriptor schemes for multikey retrieval are presented and compared. The descriptors are formed using superimposed coding techniques and stored using a bit-inversion technique. A fast-batch insertion algorithm for which the cost of forming the bit-inverted file is less than one disk access per record is presented. For large data files, it is shown that the two-level implementation is generally more efficient for queries with a small number of matchin ...

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¹ Scalable Store-Load Forwarding via Store Queue Index Prediction

window

Tingting Sha, Milo M. K. Martin, Amir Roth

November 2005 Proceedings of the 38th Annual IEEE/ACM International Symposium on Microarchitecture (MICRO'05) - Volume 00 MICRO '05

Publisher: IEEE Computer Society

Full text available: Publisher Site

Additional Information: full citation, abstract

Conventional processors use a fully-associative store queue (SQ) to implement store-load forwarding. Associative search latency does not scale well to capacities and bandwidths required by wide-issue, large window processors. In this work, we improve SQ scalability by implementing store-load forwarding using speculative indexed access rather than associative search. Our design uses prediction to identify the single SO entry from which each dynamic load is most likely to forward. When a load exec ...

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edited by Haran Boral, Pascal Faudemay - 1989 - 393 pages

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by McGraw-Hill - 2004

Page 274 - ... 5db A queue consisting of jobs that have been submitted for execution by a ... (coMmiT sd) A sequence of characters preceded by an H and a character ...

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